

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,721,299 B1
APPLICATION NO. : 09/376373
DATED : April 13, 2004
INVENTOR(S) : Young-Joon Song

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

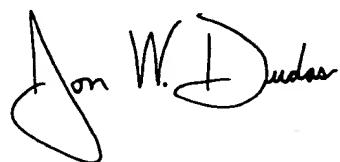
Delete Claim 36, and insert Claim 36, as shown.

36. A frame structure for a downlink Dedicated Physical Control Channel (DPCCH) in a communication system, wherein the improvement comprises each frame of the downlink DPCCH having 15 slots and N_{pilot} number of pilot bits in each slot, where $2 \leq N_{pilot} \leq 16$, and pilot bit patterns comprise at least one of the following based on N_{pilot} number of pilot bits:

Symbol #	when $N_{pilot} = 2$		when $N_{pilot} = 4$		when $N_{pilot} = 8$				when $N_{pilot} = 16$							
	0	1	0	1	2	3	0	1	2	3	4	5	6	7		
Slot #0	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
1	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
2	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
3	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
4	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
5	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
6	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
7	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
8	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
9	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
10	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
12	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
13	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11
14	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11	11

wherein pilot bit patterns are used in a communication system.

Signed and Sealed this
Fourteenth Day of October, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office